## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Shinji ONO

Title:

CODE PROCESSING CIRCUIT

Appl. No.:

10/743,497

Filing Date:

12/23/2003

Examiner:

Unassigned

Art Unit:

Unassigned

## TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

ATTENTION: DRAWING REVIEW BRANCH

Sir:

Transmitted herewith are the formal drawings (4 sheets, Figures 1-7) for the aboveidentified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date June 24, 2004

/s/ Glenn Law, Reg. No. 34,371

**FOLEY & LARDNER LLP** Customer Number: 22428

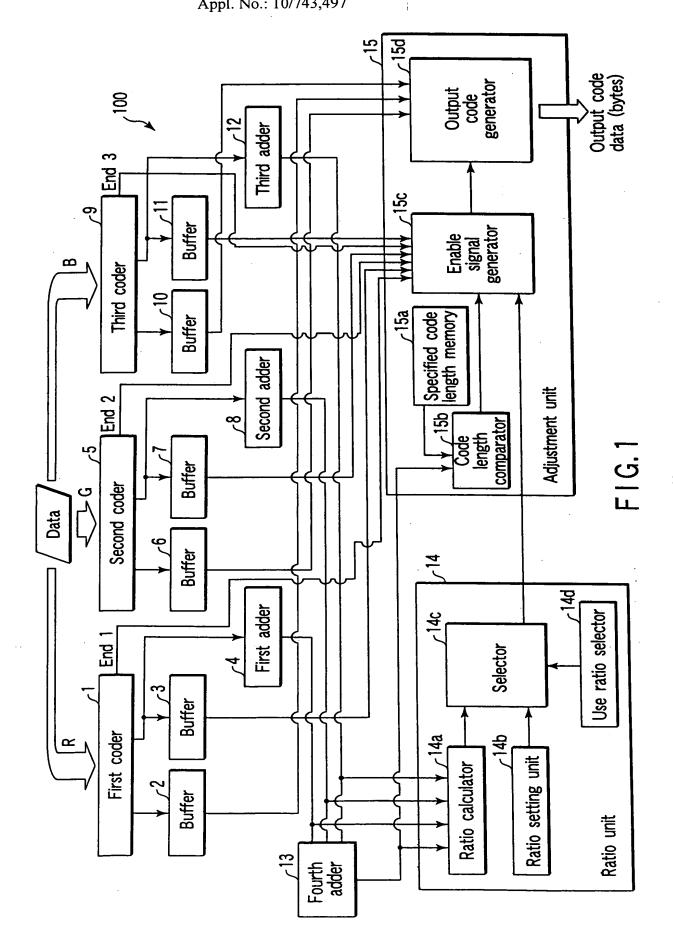
Telephone:

(202) 672-5414

Facsimile:

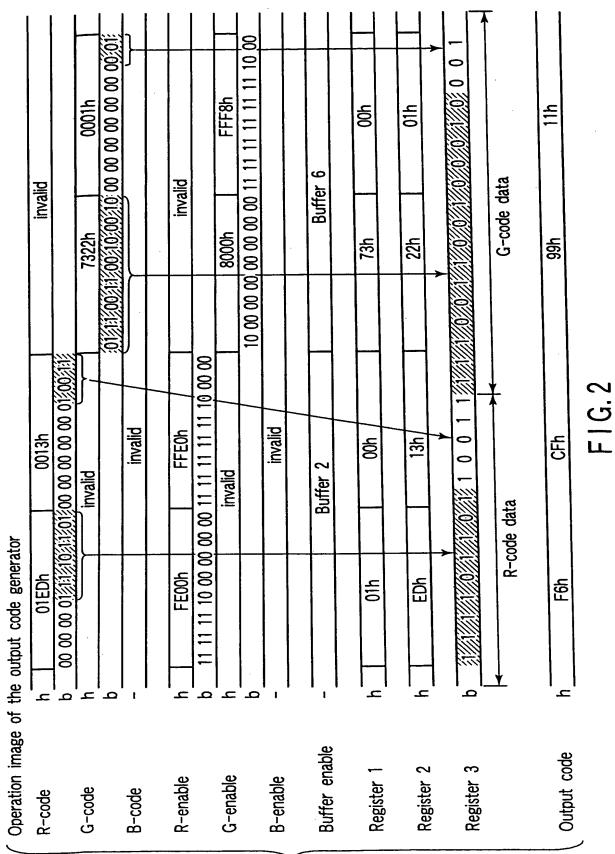
(202) 672-5399

Richard L. Schwaab Attorney for Applicant Registration No. 25,479 Title: CODE PROCESSING CIRCUIT Inventor(s): Shinji ONO Appl. No.: 10/743,497



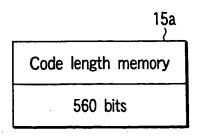
Title: CODE PROCESSING **CIRCUIT** 

Inventor(s): Shinji ONO Appl. No.: 10/743,497

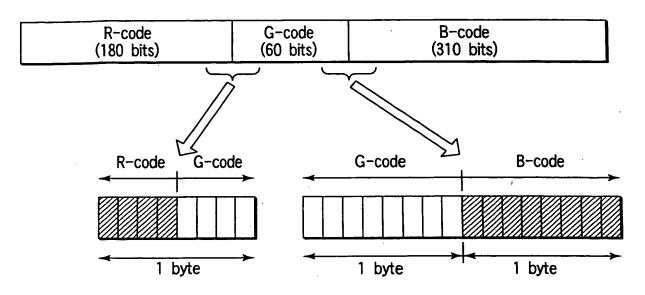


Title: CODE PROCESSING CIRCUIT

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F I G. 3



F I G. 4

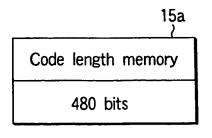


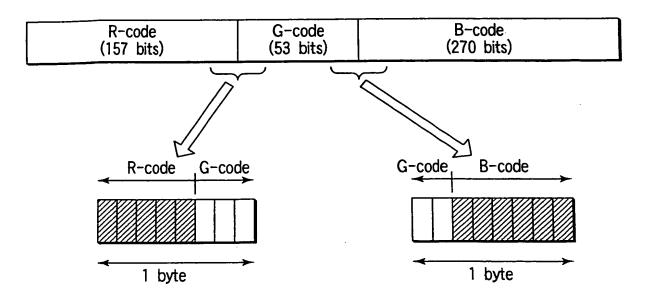
FIG.5

Title: CODE PROCESSING

CIRCUIT Inventor(s): Shinji ONO Appl. No.: 10/743,497

	R-code (180 bits)	G-code (60 bits)	B-code (310 bits)
:	:	:	:
N-6th	7 bits	8 bits	9 bits
N-5th	4 bits	5 bits	12 bits
N-4th	6 bits	10 bits	5 bits
N-3rd	13 bits	3 bits	8 bits
N-2nd	9 bits	8 bits	4 bits
N-1st	4 bits	6 bits	8 bits
Nth	10 bits.	7 bits	3 bits

F I G. 6



F1G.7